

# **AN\_Suite**

Wed Mar 17 10:18:11 1999

# I

## **Test Suite Overview**

Test Suite Structure			
<b>Suite Name</b> : AN_Suite			
<b>Standards Ref</b> :			
<b>PICS Ref</b> :			
<b>PIXIT Ref</b> :			
<b>Test Method(s)</b> :			
<b>Comments</b> :			
Test Group Reference	Selection Ref	Test Group Objective	Page Nr
Blocking_Resource/			59
Common_Error_Handling/			68
Reset_LSPId_AN_Initiated/			73
Reset_LSPId_SN_Initiated/			74
Reset_VPC_AN_Initiated/			75
Reset_VPC_SN_Initiated/			78
Shutdown_Resource/			83
StartUp/			88
Unblocking_Resource/			89
VPCI_Consistency_Check/			93
Verify_LSPId_AN_Initiated/			98
Verify_LSPId_SN_Initiated/			99
<b>Detailed Comments</b> :			

Test Case Index				
Test Group Reference	Test Case Id	Selection Ref	Description	Page Nr
Blocking_Resource/	VB5_RTMC_AN_BR_BV_01			59
Blocking_Resource/	VB5_RTMC_AN_BR_BV_02			59
Blocking_Resource/	VB5_RTMC_AN_BR_BV_03			60
Blocking_Resource/	VB5_RTMC_AN_BR_BV_04			60
Blocking_Resource/	VB5_RTMC_AN_BR_BV_05			61
Blocking_Resource/	VB5_RTMC_AN_BR_BV_06			61
Blocking_Resource/	VB5_RTMC_AN_BR_BV_07			62
Blocking_Resource/	VB5_RTMC_AN_BR_BV_08			62
Blocking_Resource/	VB5_RTMC_AN_BR_BV_11			63
Blocking_Resource/	VB5_RTMC_AN_BR_BV_12			63
Blocking_Resource/	VB5_RTMC_AN_BR_BV_13			64
Blocking_Resource/	VB5_RTMC_AN_BR_BV_14			64
Blocking_Resource/	VB5_RTMC_AN_BR_BV_15			65
Blocking_Resource/	VB5_RTMC_AN_BR_BV_16			65
Blocking_Resource/	VB5_RTMC_AN_BR_BV_17			66
Blocking_Resource/	VB5_RTMC_AN_BR_BV_18			66
Blocking_Resource/	VB5_RTMC_AN_BR_TI_01			67
Blocking_Resource/	VB5_RTMC_AN_BR_TI_11			67
Common_Error_Handling/	VB5_RTMC_AN_CEH_BI_01			68
Common_Error_Handling/	VB5_RTMC_AN_CEH_BI_02			68
Common_Error_Handling/	VB5_RTMC_AN_CEH_BI_03			69
Common_Error_Handling/	VB5_RTMC_AN_CEH_BI_04			69
Common_Error_Handling/	VB5_RTMC_AN_CEH_BI_05			70
Common_Error_Handling/	VB5_RTMC_AN_CEH_BI_06			70
Common_Error_Handling/	VB5_RTMC_AN_CEH_BI_07			71

Continued on next page

Continued from previous page

Test Case Index				
Test Group Reference	Test Case Id	Selection Ref	Description	Page Nr
Common_Error_Handling/	VB5_RTMC_AN_CEH_BI_11			71
Common_Error_Handling/	VB5_RTMC_AN_CEH_BI_12			72
Common_Error_Handling/	VB5_RTMC_AN_CEH_BI_13			72
Reset_LSPIId_AN_Initiate d/	VB5_RTMC_AN_RLA_BV_01			73
Reset_LSPIId_AN_Initiate d/	VB5_RTMC_AN_RLA_TI_01			73
Reset_LSPIId_SN_Initiate d/	VB5_RTMC_AN_RLS_BI_01			74
Reset_LSPIId_SN_Initiate d/	VB5_RTMC_AN_RLS_BV_01			74
Reset_VPC_AN_Initiated/	VB5_RTMC_AN_RVA_BV_01			75
Reset_VPC_AN_Initiated/	VB5_RTMC_AN_RVA_BV_02			75
Reset_VPC_AN_Initiated/	VB5_RTMC_AN_RVA_BV_11			76
Reset_VPC_AN_Initiated/	VB5_RTMC_AN_RVA_BV_12			76
Reset_VPC_AN_Initiated/	VB5_RTMC_AN_RVA_TI_01			77
Reset_VPC_AN_Initiated/	VB5_RTMC_AN_RVA_TI_11			77
Reset_VPC_SN_Initiated/	VB5_RTMC_AN_RVS_BI_01			78
Reset_VPC_SN_Initiated/	VB5_RTMC_AN_RVS_BI_02			78
Reset_VPC_SN_Initiated/	VB5_RTMC_AN_RVS_BI_03			79
Reset_VPC_SN_Initiated/	VB5_RTMC_AN_RVS_BI_11			79
Reset_VPC_SN_Initiated/	VB5_RTMC_AN_RVS_BI_12			80
Reset_VPC_SN_Initiated/	VB5_RTMC_AN_RVS_BI_13			80
Reset_VPC_SN_Initiated/	VB5_RTMC_AN_RVS_BV_01			81
Reset_VPC_SN_Initiated/	VB5_RTMC_AN_RVS_BV_02			81
Reset_VPC_SN_Initiated/	VB5_RTMC_AN_RVS_BV_11			82
Reset_VPC_SN_Initiated/	VB5_RTMC_AN_RVS_BV_12			82
Shutdown_Resource/	VB5_RTMC_AN_SR_BV_01			83
Shutdown_Resource/	VB5_RTMC_AN_SR_BV_02			83

Continued on next page

Continued from previous page

Test Case Index				
Test Group Reference	Test Case Id	Selection Ref	Description	Page Nr
Shutdown_Resource/	VB5_RTMC_AN_SR_BV_03			84
Shutdown_Resource/	VB5_RTMC_AN_SR_BV_04			84
Shutdown_Resource/	VB5_RTMC_AN_SR_BV_11			85
Shutdown_Resource/	VB5_RTMC_AN_SR_BV_12			85
Shutdown_Resource/	VB5_RTMC_AN_SR_BV_13			86
Shutdown_Resource/	VB5_RTMC_AN_SR_BV_14			86
Shutdown_Resource/	VB5_RTMC_AN_SR_TI_01			87
Shutdown_Resource/	VB5_RTMC_AN_SR_TI_11			87
StartUp/	VB5_RTMC_AN_ST_CA_01			88
StartUp/	VB5_RTMC_AN_ST_CA_02			88
StartUp/	VB5_RTMC_AN_ST_CA_03			89
Unblocking_Resource/	VB5_RTMC_AN_UR_BV_01			89
Unblocking_Resource/	VB5_RTMC_AN_UR_BV_02			90
Unblocking_Resource/	VB5_RTMC_AN_UR_BV_03			90
Unblocking_Resource/	VB5_RTMC_AN_UR_BV_11			91
Unblocking_Resource/	VB5_RTMC_AN_UR_BV_12			91
Unblocking_Resource/	VB5_RTMC_AN_UR_BV_13			92
Unblocking_Resource/	VB5_RTMC_AN_UR_TI_01			92
Unblocking_Resource/	VB5_RTMC_AN_UR_TI_11			93
VPCI_Consistency_Check/	VB5_RTMC_AN_CC_BI_01			93
VPCI_Consistency_Check/	VB5_RTMC_AN_CC_BI_02			94
VPCI_Consistency_Check/	VB5_RTMC_AN_CC_BI_12			94
VPCI_Consistency_Check/	VB5_RTMC_AN_CC_BO_01			95
VPCI_Consistency_Check/	VB5_RTMC_AN_CC_BO_11			95
VPCI_Consistency_Check/	VB5_RTMC_AN_CC_BV_01			96

Continued on next page

*Continued from previous page*

Test Case Index				
Test Group Reference	Test Case Id	Selection Ref	Description	Page Nr
VPCI_Consistency_Check/	VB5_RTMC_AN_CC_BV_02			96
VPCI_Consistency_Check/	VB5_RTMC_AN_CC_BV_11			97
VPCI_Consistency_Check/	VB5_RTMC_AN_CC_BV_12			97
Verify_LSPId_AN_Initiate/	VB5_RTMC_AN_VLA_BV_01			98
Verify_LSPId_AN_Initiate/	VB5_RTMC_AN_VLA_TI_01			98
Verify_LSPId_SN_Initiate/	VB5_RTMC_AN_VLS_BV_01			99
Detailed Comments :				

Test Step Index			
Test Step Group Reference	Test Step Id	Description	Page Nr
Preambles/	Startup_AN		100
Detailed Comments :			



Default Index			
Default Group Reference	Default Id	Description	Page Nr
	OtherwiseFail		101
Detailed Comments :			

## **II**

# **Declarations Part**

ASN.1 Type Definition	
Type Name	: BIT_STRING
Encoding Variation	:
Comments	:
Type Definition	
BIT STRING	
Detailed Comments :	

ASN.1 Type Definition	
Type Name	: OCTET_STRING
Encoding Variation	:
Comments	:
Type Definition	
OCTET STRING	
Detailed Comments :	

ASN.1 Type Definition	
Type Name	: IT_TransId
Encoding Variation	:
Comments	:
Type Definition	
INTEGER	
Detailed Comments :	

ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
AwaitClear	AwaitClear	RTMCMessages		
AwaitClearAck	AwaitClearAck	RTMCMessages		
AwaitClearComp	AwaitClearComp	RTMCMessages		
AwaitClearCompAck	AwaitClearCompAck	RTMCMessages		
BlockRsc	BlockRsc	RTMCMessages		
BlockRscAck	BlockRscAck	RTMCMessages		
ConsCheckEnd	ConsCheckEnd	RTMCMessages		
ConsCheckEndAck	ConsCheckEndAck	RTMCMessages		
ConsCheckReq	ConsCheckReq	RTMCMessages		
ConsCheckReqAck	ConsCheckReqAck	RTMCMessages		
LSPId	LSPId	RTMCMessages		
ProtocolError	ProtocolError	RTMCMessages		
ReqLSPId	ReqLSPId	RTMCMessages		
ResetRsc	ResetRsc	RTMCMessages		
ResetRscAck	ResetRscAck	RTMCMessages		
UnblockRsc	UnblockRsc	RTMCMessages		
UnblockRscAck	UnblockRscAck	RTMCMessages		
UnknownMessage	UnknownMessage	RTMCMessages		
CommonMsgInfo	CommonMsgInfo	RTMCMessages		
TransId	TransId	RTMCMessages		
MsgCompatInd	MsgCompatInd	RTMCMessages		
BIRscListIEs	BIRscListIEs	RTMCMessages		
RscListIEs	RscListIEs	RTMCMessages		
BIRscIdIE	BIRscIdIE	RTMCMessages		
ProtErrCauseIE	ProtErrCauseIE	RTMCMessages		
RepeatIndIE	RepeatIndIE	RTMCMessages		
ResultIE	ResultIE	RTMCMessages		
RscIdIE	RscIdIE	RTMCMessages		
CommonIEInfo	CommonIEInfo	RTMCMessages		
IECompatInd	IECompatInd	RTMCMessages		
BIRscIdContent	BIRscIdContent	RTMCMessages		
RscIdContent	RscIdContent	RTMCMessages		
ProtErrCauseContent	ProtErrCauseContent	RTMCMessages		
RepeatIndOctet	RepeatIndOctet	RTMCMessages		
ResultIndOctet	ResultIndOctet	RTMCMessages		
BIReasonIndOctet	BIReasonIndOctet	RTMCMessages		
BIRscIndOctet	BIRscIndOctet	RTMCMessages		
RscIndOctet	RscIndOctet	RTMCMessages		
RscList	RscList	RTMCMessages		
BIRscList	BIRscList	RTMCMessages		
RscId	RscId	RTMCMessages		
BIRscId	BIRscId	RTMCMessages		
CompatFlag	CompatFlag	RTMCMessages		
MsgActInd	MsgActInd	RTMCMessages		
IEActInd	IEActInd	RTMCMessages		

Continued on next page

Continued from previous page

ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
LogId	LogId	RTMCMessages		
RepeatInd	RepeatInd	RTMCMessages		
ResultInd	ResultInd	RTMCMessages		
ErrReason	ErrReason	RTMCMessages		
AdmReason	AdmReason	RTMCMessages		
RscInd	RscInd	RTMCMessages		
ACCTreatment	ACCTreatment	RTMCMessages		
UnkRscInd	UnkRscInd	RTMCMessages		
VPCI	VPCI	RTMCMessages		
ProtErrCauseVal	ProtErrCauseVal	RTMCMessages		
ProtErrCauseDiagn	ProtErrCauseDiagn	RTMCMessages		
ExtBitLast	ExtBitLast	RTMCMessages		
SpareBits4	SpareBits4	RTMCMessages		
SpareBits3	SpareBits3	RTMCMessages		
SpareBits2	SpareBits2	RTMCMessages		
SpareBits1	SpareBits1	RTMCMessages		
ProtDiscr	ProtDiscr	RTMCMessages		
TransIdFlag	TransIdFlag	RTMCMessages		
TransIdVal	TransIdVal	RTMCMessages		
MsgType	MsgType	RTMCMessages		
MsgLength	MsgLength	RTMCMessages		
IEType	IEType	RTMCMessages		
IELength	IELength	RTMCMessages		
EnvResult	EnvResult	RTMCMessages		
EnvBIReason	EnvBIReason	RTMCMessages		
Detailed Comments :				

Test Suite Parameter Declarations			
Parameter Name	Type	PICS/PIXIT Ref	Comments
PIX_LUP_address	LogId	LUP of PUP01	Default: 000101
PIX_LUP_unkaddr	LogId	Unknown LUP of PUP01	Default: 000103
PIX_LUP_VPCI_1	VPCI	First VPCI of LUP01 of PUP01	Default: 0101
PIX_LUP_VPCI_2	VPCI	2nd VPCI of LUP01 of PUP01	Default: 0102
PIX_LUP_VPCI_unk	VPCI	unknown VPCI of LUP01 of PUP01	Default: 0103
PIX_LUP_VPCI_busy	VPCI	Busy VPCI of LUP01 of PUP01	Default: 0104
PIX_LUP_VPCI_last	VPCI	Last VPCI of LUP01 of PUP01	Default: 0105
PIX_LSP_address	LogId	LSP Id	Default: 008181
PIX_LSP_unkaddr	LogId	unknown LSP Id	Default: 008183
PIX_LSP_VPCI_1	VPCI	First VPCI of LSP81 of PSP81	Default: 8181
PIX_LSP_VPCI_2	VPCI	2nd VPCI of LSP	Default: 8182
PIX_LSP_VPCI_unk	VPCI	unknown VPCI of LSP	Default: 8183
PIX_LSP_VPCI_busy	VPCI	Busy VPCI of LSP	Default: 8184
PIX_LSP_VPCI_last	VPCI	Last VPCI of LSP	Default: 8185
PIX_automatic_loopback_results	BOOLEAN	choice dictated by implementation	
PIX_T_acl	INTEGER		Default: 1000 ms
PIX_T_block	INTEGER		Default: 1000 ms
PIX_T_consreq	INTEGER		Default: 1000 ms
PIX_T_consendl	INTEGER		Default: 1000 ms
PIX_T_lspid	INTEGER		Default: 1000 ms
PIX_T_reset	INTEGER		Default: 1000 ms
PIX_T_start	INTEGER		Default: 1000 ms
PIX_T_unblock	INTEGER		Default: 1000 ms
PIX_T_supervision	INTEGER		Default: 20000 ms
Detailed Comments :			

Test Suite Constant Declarations By Reference				
Constant Name	Type	Value Reference	Module Identifier	Comments
compatFlagNotSignif	BIT_STRING	compatFlagNotSignif	RTMCMessages	
compatFlagExplicit	BIT_STRING	compatFlagExplicit	RTMCMessages	
msgActIndReject	BIT_STRING	msgActIndReject	RTMCMessages	
msgActIndDiscardAndIgnore	BIT_STRING	msgActIndDiscardAndIgnore	RTMCMessages	
msgActIndDiscardAndReport	BIT_STRING	msgActIndDiscardAndReport	RTMCMessages	
iEActIndReject	BIT_STRING	iEActIndReject	RTMCMessages	
iEActIndDiscardAndProceed	BIT_STRING	iEActIndDiscardAndProceed	RTMCMessages	
iEActIndDiscardProceedAndReport	BIT_STRING	iEActIndDiscardProceedAndReport	RTMCMessages	
iEActIndDiscardMsgAndIgnore	BIT_STRING	iEActIndDiscardMsgAndIgnore	RTMCMessages	
iEActIndDiscardMsgAndReport	BIT_STRING	iEActIndDiscardMsgAndReport	RTMCMessages	
extBitContinues	BIT_STRING	extBitContinues	RTMCMessages	
extBitLast	BIT_STRING	extBitLast	RTMCMessages	
repeatIndMultList	BIT_STRING	repeatIndMultList	RTMCMessages	
rTMCSuccess	BIT_STRING	rTMCSuccess	RTMCMessages	
rTMCUnkRsc	BIT_STRING	rTMCUnkRsc	RTMCMessages	
rTMCOpRej	BIT_STRING	rTMCOpRej	RTMCMessages	
rTMCOpFail	BIT_STRING	rTMCOpFail	RTMCMessages	
rTMCNotPerf	BIT_STRING	rTMCNotPerf	RTMCMessages	
rTMCErrNone	BIT_STRING	rTMCErrNone	RTMCMessages	
rTMCErr	BIT_STRING	rTMCErr	RTMCMessages	
rTMCAdmNone	BIT_STRING	rTMCAdmNone	RTMCMessages	
rTMCAdmPart	BIT_STRING	rTMCAdmPart	RTMCMessages	
rTMCAdmFull	BIT_STRING	rTMCAdmFull	RTMCMessages	
compLSP	BIT_STRING	compLSP	RTMCMessages	
connLSP	BIT_STRING	connLSP	RTMCMessages	
connLUP	BIT_STRING	connLUP	RTMCMessages	
logIdUnk	BIT_STRING	logIdUnk	RTMCMessages	
vpciUnk	BIT_STRING	vpciUnk	RTMCMessages	
unkMsgType	BIT_STRING	unkMsgType	RTMCMessages	
mandIEMiss	BIT_STRING	mandIEMiss	RTMCMessages	
unrecogIE	BIT_STRING	unrecogIE	RTMCMessages	
iEContErr	BIT_STRING	iEContErr	RTMCMessages	
iENotAllowed	BIT_STRING	iENotAllowed	RTMCMessages	
msgNotCompatWithProtState	BIT_STRING	msgNotCompatWithProtState	RTMCMessages	
envSuccess	BIT_STRING	envSuccess	RTMCMessages	
envUnkRsc	BIT_STRING	envUnkRsc	RTMCMessages	
envOpRejRem	BIT_STRING	envOpRejRem	RTMCMessages	
envOpFail	BIT_STRING	envOpFail	RTMCMessages	
envNotPerf	BIT_STRING	envNotPerf	RTMCMessages	

Continued on next page

*Continued from previous page*

Test Suite Constant Declarations By Reference				
Constant Name	Type	Value Reference	Module Identifier	Comments
envOpRejLoc	BIT_STRING	envOpRejLoc	RTMCMessages	
envTransErr	BIT_STRING	envTransErr	RTMCMessages	
envMismatch	BIT_STRING	envMismatch	RTMCMessages	
envSaal	BIT_STRING	envSaal	RTMCMessages	
admPartEnv	BIT_STRING	admPartEnv	RTMCMessages	
admFullEnv	BIT_STRING	admFullEnv	RTMCMessages	
errorEnv	BIT_STRING	errorEnv	RTMCMessages	
admPartErrEnv	BIT_STRING	admPartErrEnv	RTMCMessages	
admFullErrEnv	BIT_STRING	admFullErrEnv	RTMCMessages	
Detailed Comments :				



Test Case Variable Declarations			
Variable Name	Type	Value	Comments
TCV_transldVal	TransldVal		
Detailed Comments :			

PCO Type Declarations		
PCO Type	Role	Comments
CH_AN_SN_Type	LT	
Detailed Comments :		

PCO Declarations			
PCO Name	PCO Type	Role	Comments
CH_AN_SN	CH_AN_SN_Type	LT	
Detailed Comments :			

Timer Declarations			
Timer Name	Duration	Unit	Comments
T_acl	PIX_T_acl	ms	
T_block	PIX_T_block	ms	
T_consreq	PIX_T_consreq	ms	
T_consensd	PIX_T_consensd	ms	
T_lspid	PIX_T_lspid	ms	
T_reset	PIX_T_reset	ms	
T_start	PIX_T_start	ms	
T_unblock	PIX_T_unblock	ms	
T_supervision	PIX_T_supervision	ms	
Detailed Comments :			

ASN.1 ASP Type Definition
<b>ASP Name</b> : UNBLOCK_RSC_ACK <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { unblockRscAck2 UnblockRscAck }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : BLOCK_RSC_ACK <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { blockRscAck2 BlockRscAck }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : AWAIT_CLEAR_ACK <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { awaitClearAck2 AwaitClearAck }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : AWAIT_CLEAR_COMP <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { awaitClearComp2 AwaitClearComp }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : RESET_RSC <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { resetRsc2 ResetRsc }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : RESET_RSC_ACK <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { resetRscAck2 ResetRscAck }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : REQ_LSPID <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { reqLSPId2 ReqLSPId }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : LSPID <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { ISPIId2 LSPId }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : CONS_CHECK_REQ <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { consCheckReq2 ConsCheckReq }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : CONS_CHECK_END <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { consCheckEnd2 ConsCheckEnd }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : PROTOCOL_ERROR <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { protocolError2 ProtocolError }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : SIM_SN_started <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : unknownMessage <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { unknownMessage2 UnknownMessage }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : msg_too_short <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : trans_id_err <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : unknown_protdisc <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { }
<b>Detailed Comments</b> :



ASN.1 ASP Type Definition
<b>ASP Name</b> : UNBLOCK_RSC <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { unlockRsc2 UnblockRsc }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : BLOCK_RSC <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { blockRsc2 BlockRsc }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : AWAIT_CLEAR <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { awaitClear2 AwaitClear }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition
<b>ASP Name</b> : AWAIT_CLEAR_COMP_ACK <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :
Type Definition
SEQUENCE { awaitClearCompAck2 AwaitClearCompAck }
<b>Detailed Comments</b> :

ASN.1 ASP Type Definition	
<b>ASP Name</b> : CONS_CHECK_REQ_ACK <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :	
Type Definition	
SEQUENCE { consCheckReqAck2 ConsCheckReqAck }	
<b>Detailed Comments</b> :	

ASN.1 ASP Type Definition	
<b>ASP Name</b> : CONS_CHECK_END_ACK <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :	
Type Definition	
SEQUENCE { consCheckEndAck2 ConsCheckEndAck }	
<b>Detailed Comments</b> :	

ASN.1 ASP Type Definition	
<b>ASP Name</b> : Start_AN_Implementation <b>PCO Type</b> : CH_AN_SN_Type <b>Comments</b> :	
Type Definition	
SEQUENCE { }	
<b>Detailed Comments</b> :	

# **III**

## **Constraints Part**

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_Start_AN_Implementation
<b>ASP Type</b> :	Start_AN_Implementation
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
{ }	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_AWAIT_CLEAR_ACK(Trans_Id : TransIdVal)
<b>ASP Type</b> :	AWAIT_CLEAR_ACK
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
{ awaitClearAck2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '0F'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0005'H }, result { commonIEInfo { iEType '84'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, resultIndOctet { extBitLast '1'B, spareBits3 '000'B, resultInd '0000'B } }, rscList { multRscl { } } } }	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_AWAIT_CLEAR_COMP_ACK(Trans_Id : TransIdVal)
<b>ASP Type</b> :	AWAIT_CLEAR_COMP_ACK
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
{ awaitClearCompAck2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '11'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0000'H } }	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_AWAIT_CLEAR_COMP_LSP_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	AWAIT_CLEAR_COMP
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ awaitClearComp2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '10'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000A'H }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_2 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_AWAIT_CLEAR_COMP_LSP_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	AWAIT_CLEAR_COMP
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ awaitClearComp2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '10'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0023'H }, rscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B } }, multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_1 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_2 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_last } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_AWAIT_CLEAR_COMP_LUP_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	AWAIT_CLEAR_COMP
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ awaitClearComp2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '10'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000A'H }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_2 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_AWAIT_CLEAR_COMP_LUP_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	AWAIT_CLEAR_COMP
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ awaitClearComp2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '10'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0023'H }, rscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B } }, multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_2 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_last } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_AWAIT_CLEAR_LSP_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	AWAIT_CLEAR
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ awaitClear2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0E'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000A'H }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_1 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_AWAIT_CLEAR_LSP_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	AWAIT_CLEAR
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ awaitClear2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0E'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000A'H }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_2 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_AWAIT_CLEAR_LSP_003(Trans_Id : TransIdVal)
<b>ASP Type</b> :	AWAIT_CLEAR
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ awaitClear2 { commonMsgInfo { protDiscr '49'H, transId { spareBits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0E'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0023'H }, rscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B } }, multRscld { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscldContent { rscldOctet { extBitLast '1'B, unkRscld '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscld '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_1 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscldContent { rscldOctet { extBitLast '1'B, unkRscld '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscld '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_2 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscldContent { rscldOctet { extBitLast '1'B, unkRscld '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscld '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_last } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_AWAIT_CLEAR_LUP_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	AWAIT_CLEAR
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ awaitClear2 { commonMsgInfo { protDiscr '49'H, transId { spareBits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0E'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000A'H }, rscList { multRscld { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscldContent { rscldOctet { extBitLast '1'B, unkRscld '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscld '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1 } } } } }</pre>	
<b>Detailed Comments</b> :	



ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_AWAIT_CLEAR_LUP_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	AWAIT_CLEAR
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ awaitClear2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0E'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000A'H }, rscList { multRscld { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscldContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_2 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_AWAIT_CLEAR_LUP_003(Trans_Id : TransIdVal)
<b>ASP Type</b> :	AWAIT_CLEAR
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ awaitClear2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0E'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0023'H }, rscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B }, multRscld { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscldContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1 }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscldContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_2 }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscldContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_last } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_BLOCK_RSC_LSP_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	BLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ blockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '02'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000B'H }, blRscList { multBIRscld { { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0007'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '00'B, admReason '00010'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rsclnd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_1 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_BLOCK_RSC_LSP_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	BLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ blockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '02'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000B'H }, blRscList { multBIRscld { { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0007'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '00'B, admReason '00001'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rsclnd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_1 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_BLOCK_RSC_LSP_003(Trans_Id : TransIdVal)
<b>ASP Type</b> :	BLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ blockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '02'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000B'H }, blRscList { multBIRscld { { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0007'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '01'B, admReason '00000'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rsclnd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_1 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_BLOCK_RSC_LSP_004(Trans_Id : TransIdVal)
<b>ASP Type</b> :	BLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ blockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '02'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000B'H }, blRscList { multBlRscld { { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0007'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '01'B, admReason '00001'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rscld '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_1 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_BLOCK_RSC_LSP_005(Trans_Id : TransIdVal)
<b>ASP Type</b> :	BLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ blockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '02'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000B'H }, blRscList { multBlRscld { { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0007'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '01'B, admReason '00010'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rscld '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_1 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_BLOCK_RSC_LSP_006(Trans_Id : TransIdVal)
<b>ASP Type</b> :	BLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ blockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '02'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '001B'H }, blRscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B }, multBlRscld { { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0007'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '00'B, admReason '00010'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rscld '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_1 }, { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0007'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '00'B, admReason '00010'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rscld '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_2 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_BLOCK_RSC_LSP_007(Trans_Id : TransIdVal)
<b>ASP Type</b> :	BLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ blockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '02'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '001D'H }, blRscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B }, multBlRscld { { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0009'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '00'B, admReason '00010'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rscld '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_1, vpci2 PIX_LSP_VPCI_2 }, { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0007'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '00'B, admReason '00010'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rscld '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LUP_VPCI_last } }}}}}</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_BLOCK_RSC_LSP_008(Trans_Id : TransIdVal)
<b>ASP Type</b> :	BLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ blockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '02'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000D'H }, blRscList { multBlRscld { { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0009'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '00'B, admReason '00010'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rscld '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_1, vpci2 PIX_LSP_VPCI_2 } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_BLOCK_RSC_LUP_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	BLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ blockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '02'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000B'H }, blRscList { multBlRscld { { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0007'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '00'B, admReason '00010'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rscld '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1 } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_BLOCK_RSC_LUP_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	BLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ blockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '02'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000B'H }, blRscList { multBIRscld { { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0007'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '00'B, admReason '00001'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rsclnd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_BLOCK_RSC_LUP_003(Trans_Id : TransIdVal)
<b>ASP Type</b> :	BLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ blockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '02'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000B'H }, blRscList { multBIRscld { { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0007'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '01'B, admReason '00000'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rsclnd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_BLOCK_RSC_LUP_004(Trans_Id : TransIdVal)
<b>ASP Type</b> :	BLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ blockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '02'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000B'H }, blRscList { multBIRscld { { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0007'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '01'B, admReason '00001'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rsclnd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_BLOCK_RSC_LUP_005(Trans_Id : TransIdVal)
<b>ASP Type</b> :	BLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ blockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '02'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000B'H }, blRscList { multBlRscld { { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0007'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '01'B, admReason '00010'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rscld '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_BLOCK_RSC_LUP_006(Trans_Id : TransIdVal)
<b>ASP Type</b> :	BLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ blockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '02'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '001B'H }, blRscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B }, multBlRscld { { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0007'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '00'B, admReason '00010'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rscld '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1 }, { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0007'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '00'B, admReason '00010'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rscld '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_2 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_BLOCK_RSC_LUP_007(Trans_Id : TransIdVal)
<b>ASP Type</b> :	BLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ blockRsc2 { commonMsgInfo { protDiscr '49'H, transId { spareBits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '02'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '001D'H }, blRscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B }, multBlRscld { { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0009'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '00'B, admReason '00010'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rscld '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1, vpci2 PIX_LUP_VPCI_2 }, { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0007'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '00'B, admReason '00010'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rscld '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_last } }}} }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_BLOCK_RSC_LUP_008(Trans_Id : TransIdVal)
<b>ASP Type</b> :	BLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ blockRsc2 { commonMsgInfo { protDiscr '49'H, transId { spareBits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '02'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000D'H }, blRscList { multBlRscld { { commonIEInfo { iEType '80'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0009'H }, blRscldContent { blReasonIndOctet { extBitLast '1'B, errReason '00'B, admReason '00010'B }, blRscldOctet { extBitLast '1'B, spareBits4 '0000'B, rscld '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1, vpci2 PIX_LUP_VPCI_2 } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_CONS_CHECK_END_ACK_LSP_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	CONS_CHECK_END_ACK
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ consCheckEndAck2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '07'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000F'H }, result { commonIEInfo { iEType '84'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, resultIndOctet { extBitLast '1'B, spareBits3 '000'B, resultInd '0001'B } }, rscl { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rsclContent { rsclIndOctet { extBitLast '1'B, unkRsclInd '11'B, aCCTreatment '0'B, spareBits1 '0'B, rsclInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_unk } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_CONS_CHECK_END_ACK_LSP_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	CONS_CHECK_END_ACK
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ consCheckEndAck2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '07'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000F'H }, result { commonIEInfo { iEType '84'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, resultIndOctet { extBitLast '1'B, spareBits3 '000'B, resultInd '0010'B } }, rscl { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rsclContent { rsclIndOctet { extBitLast '1'B, unkRsclInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rsclInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_last } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_CONS_CHECK_END_ACK_LSP_003(Trans_Id : TransIdVal)
<b>ASP Type</b> :	CONS_CHECK_END_ACK
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ consCheckEndAck2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '07'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000F'H }, result { commonIEInfo { iEType '84'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, resultIndOctet { extBitLast '1'B, spareBits3 '000'B, resultInd '0000'B } }, rscl { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rsclContent { rsclIndOctet { extBitLast '1'B, unkRsclInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rsclInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_2 } } } }</pre>	
<b>Detailed Comments</b> :	



ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_CONS_CHECK_END_LSP_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	CONS_CHECK_END
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ consCheckEnd2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '06'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000A'H }, rscl { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rsclContent { rsclndOctet { extBitLast '1'B, unkRsclnd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rsclnd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_unk } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_CONS_CHECK_END_LSP_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	CONS_CHECK_END
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ consCheckEnd2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '06'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000A'H }, rscl { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rsclContent { rsclndOctet { extBitLast '1'B, unkRsclnd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rsclnd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_last } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_CONS_CHECK_END_LSP_003(Trans_Id : TransIdVal)
<b>ASP Type</b> :	CONS_CHECK_END
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ consCheckEnd2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '06'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000A'H }, rscl { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rsclContent { rsclndOctet { extBitLast '1'B, unkRsclnd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rsclnd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_2 } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_CONS_CHECK_REQ_ACK_LSP_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	CONS_CHECK_REQ_ACK
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ consCheckReqAck2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '05'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000F'H }, result { commonIEInfo { iEType '84'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, resultIndOctet { extBitLast '1'B, spareBits3 '000'B, resultInd '0001'B } }, rscl { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rsclContent { rsclIndOctet { extBitLast '1'B, unkRsclInd '11'B, aCCTreatment '0'B, spareBits1 '0'B, rsclInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_unk } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_CONS_CHECK_REQ_ACK_LSP_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	CONS_CHECK_REQ_ACK
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ consCheckReqAck2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '05'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000F'H }, result { commonIEInfo { iEType '84'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, resultIndOctet { extBitLast '1'B, spareBits3 '000'B, resultInd '0000'B } }, rscl { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rsclContent { rsclIndOctet { extBitLast '1'B, unkRsclInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rsclInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_2 } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_CONS_CHECK_REQ_LSP_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	CONS_CHECK_REQ
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ consCheckReq2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '04'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000A'H }, rscl { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rsclContent { rsclIndOctet { extBitLast '1'B, unkRsclInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rsclInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_unk } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_CONS_CHECK_REQ_LSP_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	CONS_CHECK_REQ
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ consCheckReq2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '04'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000A'H }, rsclId { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rsclIdContent { rsclIdOctet { extBitLast '1'B, unkRsclId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rsclId '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_2 } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_LSPID_compLSP_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	LSPID
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ ISPIId2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '09'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0008'H }, rsclId { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0004'H }, rsclIdContent { rsclIdOctet { extBitLast '1'B, unkRsclId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rsclId '000'B }, logRsc PIX_LSP_address } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_LSPID_compLSP_003(Trans_Id : TransIdVal)
<b>ASP Type</b> :	LSPID
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ ISPIId2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0010'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '09'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0008'H }, rsclId { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rsclIdContent { rsclIdOctet { extBitLast '1'B, unkRsclId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rsclId '000'B }, logRsc PIX_LSP_address } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_PROTOCOL_ERROR_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	PROTOCOL_ERROR
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ protocolError2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '0A'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '05'H }, protErrCause { commonIEInfo { iEType '08'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '01'H }, protErrCause { extBitLast '1'B, protErrCauseVal '0000001'B } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_PROTOCOL_ERROR_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	PROTOCOL_ERROR
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ protocolError2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '0A'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '5'H }, protErrCause { commonIEInfo { iEType '08'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '01'H }, protErrCause { extBitLast '1'B, protErrCauseVal '0000011'B } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_REQ_LSPID_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	REQ_LSPID
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ reqLSPId2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '08'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0000'H } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_REQ_LSPID_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	REQ_LSPID
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
{ reqLSPId2 { commonMsgInfo { protDiscr 'AA'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '08'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0000'H } } }	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_REQ_LSPID_003(Trans_Id : TransIdVal)
<b>ASP Type</b> :	REQ_LSPID
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
{ reqLSPId2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '1010'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '08'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0000'H } } }	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_REQ_LSPID_004(Trans_Id : TransIdVal)
<b>ASP Type</b> :	REQ_LSPID
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
{ reqLSPId2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '1010'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '08'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0000'H } } }	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_REQ_LSPID_005(Trans_Id : TransIdVal)
<b>ASP Type</b> :	REQ_LSPID
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
{ reqLSPId2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '08'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0000'H } } }	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_REQ_LSPID_006(Trans_Id : TransIdVal)
<b>ASP Type</b> :	REQ_LSPID
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
{ reqLSPId2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '08'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength 'FF'H } } }	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_REQ_LSPID_007(Trans_Id : TransIdVal)
<b>ASP Type</b> :	REQ_LSPID
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
{ reqLSPId2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '08'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0000'H } } }	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_REQ_LSPID_008(Trans_Id : TransIdVal)
<b>ASP Type</b>	: REQ_LSPID
<b>Derivation Path</b>	:
<b>Comments</b>	:
Constraint Value	
{ reqLSPId2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '08'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0000'H } } }	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_REQ_LSPID_009(Trans_Id : TransIdVal)
<b>ASP Type</b>	: REQ_LSPID
<b>Derivation Path</b>	:
<b>Comments</b>	:
Constraint Value	
{ reqLSPId2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '08'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0000'H } } }	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC(Trans_Id : TransIdVal)
<b>ASP Type</b>	: RESET_RSC
<b>Derivation Path</b>	:
<b>Comments</b>	:
Constraint Value	
{ resetRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0010'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0C'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0000'H }, rscList { multRscId { } } } }	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_ACK_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC_ACK
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRscAck2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '0E'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0005'H }, result { commonIEInfo { iEType '84'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, resultIndOctet { extBitLast '1'B, spareBits3 '000'B, resultInd '0000'B } }, rscList { multRscId { } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_ACK_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC_ACK
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRscAck2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0010'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '0E'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0005'H }, result { commonIEInfo { iEType '84'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, resultIndOctet { extBitLast '1'B, spareBits3 '000'B, resultInd '0000'B } }, rscList { multRscId { } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_ACK_LSP(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC_ACK
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRscAck2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '0E'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000F'H }, result { commonIEInfo { iEType '84'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, resultIndOctet { extBitLast '1'B, spareBits3 '000'B, resultInd '0001'B } }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '11'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_unk } } } }</pre>	
<b>Detailed Comments</b> :	



ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_ACK_LUP(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC_ACK
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRscAck2 { commonMsgInfo { protDiscr '49'H, transId { spareBits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '0E'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000F'H }, result { commonIEInfo { iEType '84'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, resultIndOctet { extBitLast '1'B, spareBits3 '000'B, resultInd '0001'B } }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '11'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_unk } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_ACK_complSP(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC_ACK
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRscAck2 { commonMsgInfo { protDiscr '49'H, transId { spareBits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '0E'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000D'H }, result { commonIEInfo { iEType '84'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, resultIndOctet { extBitLast '1'B, spareBits3 '000'B, resultInd '0001'B } }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0004'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '000'B }, logRsc PIX_LSP_unkaddr } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_LSP_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRsc2 { commonMsgInfo { protDiscr '49'H, transId { spareBits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0C'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000A'H }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_2 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_LSP_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0C'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0019'H }, rscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B } }, multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_1 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_2 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_LSP_003(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0C'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000A'H }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_unk } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_LSP_004(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0C'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0023'H }, rscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B } }, multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_1 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_2 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_unk } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_LSP_005(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0C'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0023'H }, rscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B } }, multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_1 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_2 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_last } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_LUP_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0C'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000C'H }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0008'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_2, vpci2 PIX_LUP_VPCI_busy } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_LUP_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0C'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000C'H }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0008'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '101'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_2, vpci2 PIX_LSP_VPCI_busy } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_LUP_003(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0C'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000A'H }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_2 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_LUP_004(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0C'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0019'H }, rscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B } }, multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_2 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_LUP_005(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0C'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000A'H }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_unk } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_LUP_006(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0C'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0023'H }, rscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B } }, multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_2 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_unk } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_LUP_007(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0C'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0023'H }, rscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B } }, multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_2 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_last } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_LUP_008(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRsc2 { commonMsgInfo { protDiscr '49'H, transId { spareBits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0C'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0023'H }, rscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B } }, multRscId { { commonIEInfo { iEType 'BB'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_unk } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_LUP_009(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRsc2 { commonMsgInfo { protDiscr '49'H, transId { spareBits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0C'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0023'H }, rscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B } }, multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '00FF'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIdOctet { extBitLast '1'B, unkRscId '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscId '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_unk } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_compLSP_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0C'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0008'H }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0004'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '000'B }, logRsc PIX_LSP_unkaddr } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_RESET_RSC_compLSP_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	RESET_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ resetRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '0C'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0008'H }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0004'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '000'B }, logRsc PIX_LSP_address } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_UNBLOCK_RSC_LSP_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	UNBLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ unblockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '12'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000C'H }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0008'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_1, vpci2 PIX_LSP_VPCI_2 } } } } }</pre>	
<b>Detailed Comments</b> :	



ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_UNBLOCK_RSC_LSP_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	UNBLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ unblockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '12'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000A'H }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_1 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_UNBLOCK_RSC_LSP_003(Trans_Id : TransIdVal)
<b>ASP Type</b> :	UNBLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ unblockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '12'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0019'H }, rscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B }, multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_1 }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '001'B }, logRsc PIX_LSP_address, vpci1 PIX_LSP_VPCI_2 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_UNBLOCK_RSC_LUP_001(Trans_Id : TransIdVal)
<b>ASP Type</b> :	UNBLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ unblockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '12'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000C'H }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0008'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1, vpci2 PIX_LUP_VPCI_2 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_UNBLOCK_RSC_LUP_002(Trans_Id : TransIdVal)
<b>ASP Type</b> :	UNBLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ unblockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '12'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '000A'H }, rscList { multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_UNBLOCK_RSC_LUP_003(Trans_Id : TransIdVal)
<b>ASP Type</b> :	UNBLOCK_RSC
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ unblockRsc2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '0'B, transIdVal Trans_Id }, msgType '12'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength '0019'H }, rscList { repeatInd { commonIEInfo { iEType '43'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0001'H }, repeatIndOctet { extBitLast '1'B, spareBits3 '000'B, repeatInd '0011'B }, multRscId { { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_1 } }, { commonIEInfo { iEType '85'H, iECompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits1 '0'B, iEActInd '000'B }, iELength '0006'H }, rscIdContent { rscIndOctet { extBitLast '1'B, unkRscInd '00'B, aCCTreatment '0'B, spareBits1 '0'B, rscInd '101'B }, logRsc PIX_LUP_address, vpci1 PIX_LUP_VPCI_2 } } } } }</pre>	
<b>Detailed Comments</b> :	

ASN.1 ASP Constraint Declaration	
<b>Constraint Name</b> :	c_unknownMessage(Trans_Id : TransIdVal)
<b>ASP Type</b> :	unknownMessage
<b>Derivation Path</b> :	
<b>Comments</b> :	
Constraint Value	
<pre>{ unknownMessage2 { commonMsgInfo { protDiscr '49'H, transId { sparebits4 '0000'B, lengthOfTransIdVal '0011'B, transIdFlag '1'B, transIdVal Trans_Id }, msgType '55'H, msgCompatInd { extBitLast '1'B, spareBits2 '00'B, compatFlag '0'B, spareBits21 '00'B, msgActInd '00'B }, msgLength 'H' } }</pre>	
<b>Detailed Comments</b> :	

# **IV**

## **Dynamic Part**

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_BV_01 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LUP_001(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LUP_001(? )	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_BV_02 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LUP_002(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LUP_002(? )	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_BV_03 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LUP_003(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LUP_003(? )	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_BV_04 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LUP_004(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LUP_004(? )	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_BV_05 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LUP_005(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LUP_005(? )	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_BV_06 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LUP_006(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LUP_006(? )	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_BV_07 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LUP_008(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LUP_008(? )	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_BV_08 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LUP_007(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LUP_007(? )	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_BV_11 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LSP_001(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LSP_001(? )	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_BV_12 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LSP_002(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LSP_002(? )	PASS	
Detailed Comments :					



Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_BV_13 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LSP_003(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LSP_003(? )	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_BV_14 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LSP_004(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LSP_004(? )	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_BV_15 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LSP_005(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LSP_005(? )	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_BV_16 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LSP_006(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LSP_006(? )	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_BV_17 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LSP_008(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LSP_008(? )	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_BV_18 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LSP_007(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LSP_007(? )	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_TI_01 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LUP_001(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LUP_001(? )		
4		START T_block			
5		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LUP_001(? )	PASS	
6		? TIMEOUT T_block		FAIL	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_BR_TI_11 <b>Group</b> : Blocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! BLOCK_RSC>	c_BLOCK_RSC_LSP_001(? )		
3		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LSP_001(? )		
4		START T_block			
5		CH_AN_SN ? BLOCK_RSC	c_BLOCK_RSC_LSP_001(? )	PASS	
6		? TIMEOUT T_block		FAIL	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CEH_BI_01 <b>Group</b> : Common_Error_Handling/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! REQ_LSPID	c_REQ_LSPID_002('0000000000001100101'B)		
3		CH_AN_SN ! REQ_LSPID	c_REQ_LSPID_001('0000000000001100110'B)		
4		CH_AN_SN ? LSPID	c_LSPID_compLSP_002('00000000000000001100110'B)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CEH_BI_02 <b>Group</b> : Common_Error_Handling/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! REQ_LSPID	c_REQ_LSPID_003('0000000000001100101'B)		
3		CH_AN_SN ! REQ_LSPID	c_REQ_LSPID_001('0000000000001100110'B)		
4		CH_AN_SN ? LSPID	c_LSPID_compLSP_002('00000000000000001100110'B)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CEH_BI_03 <b>Group</b> : Common_Error_Handling/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! REQ_LSPID	c_REQ_LSPID_004('0000000000001100101'B)		
3		CH_AN_SN ! REQ_LSPID	c_REQ_LSPID_001('0000000000001100110'B)		
4		CH_AN_SN ? LSPID	c_LSPID_compLSP_002('00000000000000001100110'B)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CEH_BI_04 <b>Group</b> : Common_Error_Handling/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! REQ_LSPID	c_REQ_LSPID_006('0000000000001100101'B)		
3		CH_AN_SN ! REQ_LSPID	c_REQ_LSPID_005('0000000000001100110'B)		
4		CH_AN_SN ? LSPID	c_LSPID_compLSP_002('00000000000000001100110'B)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CEH_BI_05 <b>Group</b> : Common_Error_Handling/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! REQ_LSPID>	c_REQ_LSPID_001(?)		
3		CH_AN_SN ? REQ_LSPID (TCV_transIdVal := REQ_LSPID.reqLSPId2.commonMsgInfo .transId.transIdVal)	c_REQ_LSPID_001(?)		
4		CH_AN_SN ! LSPID	c_LSPID_compLSP_002('00 000000000000000111111' B)		
5		CH_AN_SN ? REQ_LSPID (TCV_transIdVal := REQ_LSPID.reqLSPId2.commonMsgI nfo.transId.transIdVal)	c_REQ_LSPID_001(?)		
6		CH_AN_SN ! LSPID	c_LSPID_compLSP_002(TC V_transIdVal)	(PASS)	
7		CH_AN_SN ! REQ_LSPID	c_REQ_LSPID_001('00000 000000000001100101'B)		
8		CH_AN_SN ? LSPID	c_LSPID_compLSP_002('00 0000000000000001100101' B)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CEH_BI_06 <b>Group</b> : Common_Error_Handling/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! REQ_LSPID	c_REQ_LSPID_008('00000 000000000001100101'B)		
3		CH_AN_SN ! REQ_LSPID	c_REQ_LSPID_007('00000 000000000001100110'B)		
4		CH_AN_SN ? LSPID	c_LSPID_compLSP_002('00 0000000000000001100110' B)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CEH_BI_07 <b>Group</b> : Common_Error_Handling/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! unknownMessage	c_unknownMessage('0000000000001100101'B)		
3		CH_AN_SN ? PROTOCOL_ERROR	c_PROTOCOL_ERROR_001('00000000000000001100101'B)	(PASS)	
4		CH_AN_SN ! REQ_LSPID	c_REQ_LSPID_009('0000000000001100110'B)		
5		CH_AN_SN ? LSPID	c_LSPID_compLSP_002('00000000000000001100110'B)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CEH_BI_11 <b>Group</b> : Common_Error_Handling/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! RESET_RSC	c_RESET_RSC('0000000000001100101'B)		
3		CH_AN_SN ? PROTOCOL_ERROR	c_PROTOCOL_ERROR_002('00000000000000001100101'B)	PASS	
<b>Detailed Comments</b> :					



Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CEH_BI_12 <b>Group</b> : Common_Error_Handling/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! RESET_RSC	c_RESET_RSC_LUP_008('0000000000000001100101'B)		
3		CH_AN_SN ? RESET_RSC_ACK	c_RESET_RSC_ACK_LUP('0000000000000001100101'B)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CEH_BI_13 <b>Group</b> : Common_Error_Handling/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! RESET_RSC	c_RESET_RSC_LUP_009('0000000000000001100101'B)		
3		CH_AN_SN ? RESET_RSC_ACK	c_RESET_RSC_ACK_LUP('0000000000000001100101'B)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RLA_BV_01 <b>Group</b> : Reset_LSPIId_AN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! RESET_RSC>	c_RESET_RSC_compLSP_002(?)		
3		CH_AN_SN ? RESET_RSC	c_RESET_RSC_compLSP_002(?)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RLA_TI_01 <b>Group</b> : Reset_LSPIId_AN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! RESET_RSC>	c_RESET_RSC_compLSP_002(?)		
3		CH_AN_SN ? RESET_RSC	c_RESET_RSC_compLSP_002(?)		
4		START T_reset			
5		CH_AN_SN ? RESET_RSC	c_RESET_RSC_compLSP_002(?)	PASS	
6		? TIMEOUT T_reset		FAIL	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RLS_BI_01 <b>Group</b> : Reset_LSPIId_SN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! RESET_RSC	c_RESET_RSC_compLSP_001('00000000000000001100101'B)		
3		CH_AN_SN ? RESET_RSC_ACK	c_RESET_RSC_ACK_compLSP('00000000000000001100101'B)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RLS_BV_01 <b>Group</b> : Reset_LSPIId_SN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! RESET_RSC	c_RESET_RSC_compLSP_002('00000000000000001100101'B)		
3		CH_AN_SN ? RESET_RSC_ACK	c_RESET_RSC_ACK_001('00000000000000001100101'B)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RVA_BV_01 <b>Group</b> : Reset_VPC_AN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! RESET_RSC>	c_RESET_RSC_LUP_003(? )		
3		CH_AN_SN ? RESET_RSC	c_RESET_RSC_LUP_003(? )	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RVA_BV_02 <b>Group</b> : Reset_VPC_AN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! RESET_RSC>	c_RESET_RSC_LUP_004(? )		
3		CH_AN_SN ? RESET_RSC	c_RESET_RSC_LUP_004(? )	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RVA_BV_11 <b>Group</b> : Reset_VPC_AN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! RESET_RSC>	c_RESET_RSC_LSP_001(? )		
3		CH_AN_SN ? RESET_RSC	c_RESET_RSC_LSP_001(? )	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RVA_BV_12 <b>Group</b> : Reset_VPC_AN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! RESET_RSC>	c_RESET_RSC_LSP_002(? )		
3		CH_AN_SN ? RESET_RSC	c_RESET_RSC_LSP_002(? )	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RVA_TI_01 <b>Group</b> : Reset_VPC_AN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! RESET_RSC>	c_RESET_RSC_LUP_003(? )		
3		CH_AN_SN ? RESET_RSC	c_RESET_RSC_LUP_003(? )		
4		START T_reset			
5		CH_AN_SN ? RESET_RSC	c_RESET_RSC_LUP_003(? )	PASS	
6		? TIMEOUT T_reset		FAIL	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RVA_TI_11 <b>Group</b> : Reset_VPC_AN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! RESET_RSC>	c_RESET_RSC_LSP_001(? )		
3		CH_AN_SN ? RESET_RSC	c_RESET_RSC_LSP_001(? )		
4		START T_reset			
5		CH_AN_SN ? RESET_RSC	c_RESET_RSC_LSP_001(? )	PASS	
6		? TIMEOUT T_reset		FAIL	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RVS_BI_01 <b>Group</b> : Reset_VPC_SN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! RESET_RSC	c_RESET_RSC_LUP_005('0000000000000001100101'B)		
3		CH_AN_SN ? RESET_RSC_ACK	c_RESET_RSC_ACK_LUP('0000000000000001100101'B)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RVS_BI_02 <b>Group</b> : Reset_VPC_SN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! RESET_RSC	c_RESET_RSC_LUP_006('0000000000000001100101'B)		
3		CH_AN_SN ? RESET_RSC_ACK	c_RESET_RSC_ACK_LUP('0000000000000001100101'B)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RVS_BI_03 <b>Group</b> : Reset_VPC_SN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! RESET_RSC	c_RESET_RSC_LUP_001(T CV_transIdVal)		
3		CH_AN_SN ? RESET_RSC_ACK	c_RESET_RSC_ACK_LUP( ?)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RVS_BI_11 <b>Group</b> : Reset_VPC_SN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! RESET_RSC	c_RESET_RSC_LSP_003('0 0000000000000001100101 'B)		
3		CH_AN_SN ? RESET_RSC_ACK	c_RESET_RSC_ACK_LSP('0 000000000000000110010 1'B)	PASS	
<b>Detailed Comments</b> :					



Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RVS_BI_12 <b>Group</b> : Reset_VPC_SN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! RESET_RSC	c_RESET_RSC_LSP_004('0000000000000001100101'B)		
3		CH_AN_SN ? RESET_RSC_ACK	c_RESET_RSC_ACK_LSP('0000000000000001100101'B)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RVS_BI_13 <b>Group</b> : Reset_VPC_SN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! RESET_RSC	c_RESET_RSC_LUP_002(TCV_transIdVal)		
3		CH_AN_SN ? RESET_RSC_ACK	c_RESET_RSC_ACK_LSP(?)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RVS_BV_01 <b>Group</b> : Reset_VPC_SN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! RESET_RSC	c_RESET_RSC_LUP_003('0000000000000001100101'B)		
3		CH_AN_SN ? RESET_RSC_ACK	c_RESET_RSC_ACK_001('0000000000000001100101'B)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RVS_BV_02 <b>Group</b> : Reset_VPC_SN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! RESET_RSC	c_RESET_RSC_LUP_007('0000000000000001100101'B)		
3		CH_AN_SN ? RESET_RSC_ACK	c_RESET_RSC_ACK_001('0000000000000001100101'B)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RVS_BV_11 <b>Group</b> : Reset_VPC_SN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! RESET_RSC	c_RESET_RSC_LSP_001('0000000000000001100101'B)		
3		CH_AN_SN ? RESET_RSC_ACK	c_RESET_RSC_ACK_001('0000000000000001100101'B)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_RVS_BV_12 <b>Group</b> : Reset_VPC_SN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! RESET_RSC	c_RESET_RSC_LSP_005('0000000000000001100101'B)		
3		CH_AN_SN ? RESET_RSC_ACK	c_RESET_RSC_ACK_001('0000000000000001100101'B)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_SR_BV_01 <b>Group</b> : Shutdown_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! AWAIT_CLEAR>	c_AWAIT_CLEAR_LUP_001 (?)		
3		CH_AN_SN ? AWAIT_CLEAR	c_AWAIT_CLEAR_LUP_001 (?)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_SR_BV_02 <b>Group</b> : Shutdown_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! AWAIT_CLEAR>	c_AWAIT_CLEAR_LUP_002 (?)		
3		CH_AN_SN ? AWAIT_CLEAR (TCV_transIdVal := AWAIT_CLEAR.awaitClear2.commonMsg Info.transId.transIdVal)	c_AWAIT_CLEAR_LUP_002 (?)		
4		CH_AN_SN ! AWAIT_CLEAR_ACK	c_AWAIT_CLEAR_ACK(TCV _transIdVal)		
5		CH_AN_SN ! AWAIT_CLEAR_COMP	c_AWAIT_CLEAR_COMP_L UP_001('0000000000000000 01100101'B)		
6		CH_AN_SN ? AWAIT_CLEAR_COMP_ACK	c_AWAIT_CLEAR_COMP_A CK('0000000000000000110 0101'B)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_SR_BV_03 <b>Group</b> : Shutdown_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! AWAIT_CLEAR>	c_AWAIT_CLEAR_LUP_003 (?)		
3		CH_AN_SN ? AWAIT_CLEAR	c_AWAIT_CLEAR_LUP_003 (?)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_SR_BV_04 <b>Group</b> : Shutdown_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! AWAIT_CLEAR>	c_AWAIT_CLEAR_LUP_003 (?)		
3		CH_AN_SN ? AWAIT_CLEAR (TCV_transIdVal := AWAIT_CLEAR.awaitClear2.commonMsg Info.transId.transIdVal)	c_AWAIT_CLEAR_LUP_003 (?)		
4		CH_AN_SN ! AWAIT_CLEAR_ACK	c_AWAIT_CLEAR_ACK(TCV _transIdVal)		
5		CH_AN_SN ! AWAIT_CLEAR_COMP	c_AWAIT_CLEAR_COMP_L UP_002('0000000000000000 01100101'B)		
6		CH_AN_SN ? AWAIT_CLEAR_COMP_ACK	c_AWAIT_CLEAR_COMP_A CK('0000000000000000110 0101'B)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_SR_BV_11 <b>Group</b> : Shutdown_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! AWAIT_CLEAR>	c_AWAIT_CLEAR_LSP_001 (?)		
3		CH_AN_SN ? AWAIT_CLEAR	c_AWAIT_CLEAR_LSP_001 (?)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_SR_BV_12 <b>Group</b> : Shutdown_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! AWAIT_CLEAR>	c_AWAIT_CLEAR_LSP_002 (?)		
3		CH_AN_SN ? AWAIT_CLEAR (TCV_transIdVal := AWAIT_CLEAR.awaitClear2.commonMsg Info.transId.transIdVal)	c_AWAIT_CLEAR_LSP_002 (?)		
4		CH_AN_SN ! AWAIT_CLEAR_ACK	c_AWAIT_CLEAR_ACK(TCV _transIdVal)		
5		CH_AN_SN ! AWAIT_CLEAR_COMP	c_AWAIT_CLEAR_COMP_L SP_001('0000000000000000 01100101'B)		
6		CH_AN_SN ? AWAIT_CLEAR_COMP_ACK	c_AWAIT_CLEAR_COMP_A CK('0000000000000000110 0101'B)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_SR_BV_13 <b>Group</b> : Shutdown_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! AWAIT_CLEAR>	c_AWAIT_CLEAR_LSP_003 (?)		
3		CH_AN_SN ? AWAIT_CLEAR	c_AWAIT_CLEAR_LSP_003 (?)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_SR_BV_14 <b>Group</b> : Shutdown_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! AWAIT_CLEAR>	c_AWAIT_CLEAR_LSP_003 (?)		
3		CH_AN_SN ? AWAIT_CLEAR (TCV_transIdVal := AWAIT_CLEAR.awaitClear2.commonMsg Info.transId.transIdVal)	c_AWAIT_CLEAR_LSP_003 (?)		
4		CH_AN_SN ! AWAIT_CLEAR_ACK	c_AWAIT_CLEAR_ACK(TCV _transIdVal)		
5		CH_AN_SN ! AWAIT_CLEAR_COMP	c_AWAIT_CLEAR_COMP_L SP_002('0000000000000000 01100101'B)		
6		CH_AN_SN ? AWAIT_CLEAR_COMP_ACK	c_AWAIT_CLEAR_COMP_A CK('0000000000000000110 0101'B)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_SR_TI_01 <b>Group</b> : Shutdown_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! AWAIT_CLEAR>	c_AWAIT_CLEAR_LUP_001 (?)		
3		CH_AN_SN ? AWAIT_CLEAR	c_AWAIT_CLEAR_LUP_001 (?)		
4		START T_acl			
5		CH_AN_SN ? AWAIT_CLEAR	c_AWAIT_CLEAR_LUP_001 (?)	PASS	
6		? TIMEOUT T_acl		FAIL	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_SR_TI_11 <b>Group</b> : Shutdown_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! AWAIT_CLEAR>	c_AWAIT_CLEAR_LSP_001 (?)		
3		CH_AN_SN ? AWAIT_CLEAR	c_AWAIT_CLEAR_LSP_001 (?)		
4		START T_acl			
5		CH_AN_SN ? AWAIT_CLEAR	c_AWAIT_CLEAR_LSP_001 (?)	PASS	
6		? TIMEOUT T_acl		FAIL	
<b>Detailed Comments</b> :					



Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_ST_CA_01 <b>Group</b> : StartUp/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		<IUT ! Start_AN_Implementation>	c_Start_AN_Implementation		
2		START T_supervision			
3		CH_AN_SN ? REQ_LSPID	c_REQ_LSPID_001(?)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_ST_CA_02 <b>Group</b> : StartUp/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		<IUT ! Start_AN_Implementation>	c_Start_AN_Implementation		
2		START T_supervision			
3		CH_AN_SN ? REQ_LSPID (TCV_transIdVal := REQ_LSPID.reqLSPId2.commonMsgInfo .transId.transIdVal)	c_REQ_LSPID_001(?)		
4		CH_AN_SN ! LSPID	c_LSPID_compLSP_002(TC V_transIdVal)		
5		CH_AN_SN ? RESET_RSC	c_RESET_RSC_compLSP_ 002(?)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_ST_CA_03 <b>Group</b> : StartUp/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! REQ_LSPID	c_REQ_LSPID_001('00000 000000000001100101'B)		
3		CH_AN_SN ? LSPID	c_LSPID_compLSP_002('00 00000000000001100101' B)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_UR_BV_01 <b>Group</b> : Unblocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! UNBLOCK_RSC>	c_UNBLOCK_RSC_LUP_00 2(?)		
3		CH_AN_SN ? UNBLOCK_RSC	c_UNBLOCK_RSC_LUP_00 2(?)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_UR_BV_02 <b>Group</b> : Unblocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! UNBLOCK_RSC>	c_UNBLOCK_RSC_LUP_00 3(?)		
3		CH_AN_SN ? UNBLOCK_RSC	c_UNBLOCK_RSC_LUP_00 3(?)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_UR_BV_03 <b>Group</b> : Unblocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! UNBLOCK_RSC>	c_UNBLOCK_RSC_LUP_00 1(?)		
3		CH_AN_SN ? UNBLOCK_RSC	c_UNBLOCK_RSC_LUP_00 1(?)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_UR_BV_11 <b>Group</b> : Unblocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! UNBLOCK_RSC>	c_UNBLOCK_RSC_LSP_00 2(?)		
3		CH_AN_SN ? UNBLOCK_RSC	c_UNBLOCK_RSC_LSP_00 2(?)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_UR_BV_12 <b>Group</b> : Unblocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! UNBLOCK_RSC>	c_UNBLOCK_RSC_LSP_00 3(?)		
3		CH_AN_SN ? UNBLOCK_RSC	c_UNBLOCK_RSC_LSP_00 3(?)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_UR_BV_13 <b>Group</b> : Unblocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! UNBLOCK_RSC>	c_UNBLOCK_RSC_LSP_00 1(?)		
3		CH_AN_SN ? UNBLOCK_RSC	c_UNBLOCK_RSC_LSP_00 1(?)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_UR_TI_01 <b>Group</b> : Unblocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! UNBLOCK_RSC>	c_UNBLOCK_RSC_LUP_00 2(?)		
3		CH_AN_SN ? UNBLOCK_RSC	c_UNBLOCK_RSC_LUP_00 2(?)		
4		START T_unblock			
5		CH_AN_SN ? UNBLOCK_RSC	c_UNBLOCK_RSC_LUP_00 2(?)	PASS	
6		? TIMEOUT T_unblock		FAIL	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_UR_TI_11 <b>Group</b> : Unblocking_Resource/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! UNBLOCK_RSC>	c_UNBLOCK_RSC_LSP_002(?)		
3		CH_AN_SN ? UNBLOCK_RSC	c_UNBLOCK_RSC_LSP_002(?)		
4		START T_unblock			
5		CH_AN_SN ? UNBLOCK_RSC	c_UNBLOCK_RSC_LSP_002(?)	PASS	
6		? TIMEOUT T_unblock		FAIL	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CC_BI_01 <b>Group</b> : VPCI_Consistency_Check/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! CONS_CHECK_REQ	c_CONS_CHECK_REQ_LSP_001('00000000000000001100101'B)		
3		CH_AN_SN ? CONS_CHECK_REQ_ACK	c_CONS_CHECK_REQ_ACK_LSP_001('00000000000000001100101'B)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CC_BI_02 <b>Group</b> : VPCI_Consistency_Check/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! CONS_CHECK_REQ	c_CONS_CHECK_REQ_LS P_002('0000000000000000 1100101'B)		
3		CH_AN_SN ? CONS_CHECK_REQ_ACK	c_CONS_CHECK_REQ_AC K_LSP_002('000000000000 000001100101'B)		
4		CH_AN_SN ! CONS_CHECK_END	c_CONS_CHECK_END_LS P_001('0000000000000000 1100110'B)		
5		CH_AN_SN ? CONS_CHECK_END_ACK	c_CONS_CHECK_END_AC K_LSP_001('000000000000 000001100110'B)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CC_BI_12 <b>Group</b> : VPCI_Consistency_Check/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! CONS_CHECK_REQ	c_CONS_CHECK_REQ_LS P_002('0000000000000000 1100101'B)		
3		<IUT ! CONS_CHECK_REQ_ACK>	c_CONS_CHECK_REQ_AC K_LSP_002(?)		
4		CH_AN_SN ? CONS_CHECK_REQ_ACK	c_CONS_CHECK_REQ_AC K_LSP_002('000000000000 000001100101'B)		
5		CH_AN_SN ! CONS_CHECK_END	c_CONS_CHECK_END_LS P_001('0000000000000000 1100110'B)		
6		CH_AN_SN ? CONS_CHECK_END_ACK	c_CONS_CHECK_END_AC K_LSP_001('000000000000 000001100110'B)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CC_BO_01 <b>Group</b> : VPCI_Consistency_Check/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! CONS_CHECK_REQ	c_CONS_CHECK_REQ_LS P_002('0000000000000000 1100101'B)		
3		CH_AN_SN ? CONS_CHECK_REQ_ACK	c_CONS_CHECK_REQ_AC K_LSP_002('000000000000 000001100101'B)		
4		CH_AN_SN ! CONS_CHECK_END	c_CONS_CHECK_END_LS P_002('0000000000000000 1100110'B)		
5		CH_AN_SN ? CONS_CHECK_END_ACK	c_CONS_CHECK_END_AC K_LSP_002('000000000000 000001100110'B)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CC_BO_11 <b>Group</b> : VPCI_Consistency_Check/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! CONS_CHECK_REQ	c_CONS_CHECK_REQ_LS P_002('0000000000000000 1100101'B)		
3		<IUT ! CONS_CHECK_REQ_ACK>	c_CONS_CHECK_REQ_AC K_LSP_002(?)		
4		CH_AN_SN ? CONS_CHECK_REQ_ACK	c_CONS_CHECK_REQ_AC K_LSP_002('000000000000 000001100101'B)		
5		CH_AN_SN ! CONS_CHECK_END	c_CONS_CHECK_END_LS P_002(?)		
6		<IUT ! CONS_CHECK_END_ACK>	c_CONS_CHECK_END_AC K_LSP_002('000000000000 000001100110'B)		
7		CH_AN_SN ? CONS_CHECK_END_ACK	c_CONS_CHECK_END_AC K_LSP_002('000000000000 000001100110'B)	PASS	
<b>Detailed Comments</b> :					



Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CC_BV_01 <b>Group</b> : VPCI_Consistency_Check/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! CONS_CHECK_REQ	c_CONS_CHECK_REQ_LS P_002('0000000000000000 1100101'B)		
3		CH_AN_SN ? CONS_CHECK_REQ_ACK	c_CONS_CHECK_REQ_AC K_LSP_002('000000000000 000001100101'B)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CC_BV_02 <b>Group</b> : VPCI_Consistency_Check/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! CONS_CHECK_REQ	c_CONS_CHECK_REQ_LS P_002('0000000000000000 1100101'B)		
3		CH_AN_SN ? CONS_CHECK_REQ_ACK	c_CONS_CHECK_REQ_AC K_LSP_002('000000000000 000001100101'B)		
4		CH_AN_SN ! CONS_CHECK_END	c_CONS_CHECK_END_LS P_003('0000000000000000 1100110'B)		
5		CH_AN_SN ? CONS_CHECK_END_ACK	c_CONS_CHECK_END_AC K_LSP_003('000000000000 000001100110'B)	PASS	
Detailed Comments :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CC_BV_11 <b>Group</b> : VPCI_Consistency_Check/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! CONS_CHECK_REQ	c_CONS_CHECK_REQ_LS P_002('0000000000000000 1100101'B)		
3		<IUT ! CONS_CHECK_REQ_ACK>	c_CONS_CHECK_REQ_AC K_LSP_002(?)		
4		CH_AN_SN ? CONS_CHECK_REQ_ACK	c_CONS_CHECK_REQ_AC K_LSP_002('0000000000 000001100101'B)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_CC_BV_12 <b>Group</b> : VPCI_Consistency_Check/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! CONS_CHECK_REQ	c_CONS_CHECK_REQ_LS P_002('0000000000000000 1100101'B)		
3		<IUT ! CONS_CHECK_REQ_ACK>	c_CONS_CHECK_REQ_AC K_LSP_002(?)		
4		CH_AN_SN ? CONS_CHECK_REQ_ACK	c_CONS_CHECK_REQ_AC K_LSP_002('0000000000 000001100101'B)		
5		CH_AN_SN ! CONS_CHECK_END	c_CONS_CHECK_END_LS P_003('0000000000000000 1100110'B)		
6		<IUT ! CONS_CHECK_END_ACK>	c_CONS_CHECK_END_AC K_LSP_003(?)		
7		CH_AN_SN ? CONS_CHECK_END_ACK	c_CONS_CHECK_END_AC K_LSP_003('0000000000 000001100110'B)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_VLA_BV_01 <b>Group</b> : Verify_LSPId_AN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! REQ_LSPID>	c_REQ_LSPID_001(?)		
3		CH_AN_SN ? REQ_LSPID	c_REQ_LSPID_001(?)	PASS	
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_VLA_TI_01 <b>Group</b> : Verify_LSPId_AN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		<IUT ! REQ_LSPID>	c_REQ_LSPID_001(?)		
3		CH_AN_SN ? REQ_LSPID	c_REQ_LSPID_001(?)		
4		START T_Ispid			
5		CH_AN_SN ? REQ_LSPID	c_REQ_LSPID_001(?)		
6		? TIMEOUT T_Ispid			
<b>Detailed Comments</b> :					

Test Case Dynamic Behaviour					
<b>Test Case Name</b> : VB5_RTMC_AN_VLS_BV_01 <b>Group</b> : Verify_LSPID_SN_Initiated/ <b>Purpose</b> : <b>Configuration</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+Startup_AN			
2		CH_AN_SN ! REQ_LSPID	c_REQ_LSPID_001('00000 000000000001100101'B)		
3		CH_AN_SN ? LSPID	c_LSPID_compLSP_002('00 00000000000001100101' B)	PASS	
<b>Detailed Comments</b> :					

Test Step Dynamic Behaviour					
<b>Test Step Name</b> : Startup_AN <b>Group</b> : Preambles/ <b>Objective</b> : <b>Default</b> : OtherwiseFail <b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		<IUT ! Start_AN_Implementation>	c_Start_AN_Implementation		
2		START T_supervision			
3		CH_AN_SN ? REQ_LSPID (TCV_transIdVal := REQ_LSPID.reqLSPId2.commonMsgInfo .transId.transIdVal)	c_REQ_LSPID_001(?)		
4		CH_AN_SN ! LSPID	c_LSPID_compLSP_003(TC V_transIdVal)		
5		CH_AN_SN ? RESET_RSC (TCV_transIdVal := RESET_RSC.resetRsc2.commonMsgI nfo.transId.transIdVal)	c_RESET_RSC_compLSP_ 002(?)		
6		CH_AN_SN ! RESET_RSC_ACK	c_RESET_RSC_ACK_002(T CV_transIdVal)		
<b>Detailed Comments</b> :					

Default Dynamic Behaviour					
<b>Default Name</b> : OtherwiseFail					
<b>Group</b> :					
<b>Objective</b> :					
<b>Comments</b> :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CH_AN_SN ?OTHERWISE		FAIL	
2		?TIMEOUT		INCONC	
<b>Detailed Comments</b> :					